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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10051916	FILING DATE 01/17/2002	CLASS 331	SUBCLASS 176	GAU 2817	EXAMINER Shirgerson
**APPLICANTS: Song Ki-Hwan; Song Ho-Sung;					
**CONTINUING DATA VERIFIED: YES NONE					
** FOREIGN APPLICATIONS VERIFIED: REPUBLIC OF KOREA 2001-30522 05/31/2001 MRJ					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials MRJ				ATTORNEY DOCKET NO 5649-947	
TITLE : Clock generation circuits and integrated circuit memory devices for controlling a clock period based on temperature and methods for using the same					
U.S. DEPT. OF COMM./PAT & TM-PTO-435L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drawg.	Figs. Drawg.
		Print Fig.	
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		Applicati n Examiner	
		PREPARED FOR ISSUE	
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